Implementation Conformance Statement
One form for each set of sample(s) is to be completed

Information for test setup	Profile	Applicant declaration

Access control applied:
• Plain text
• Basic Access Control (BAC)
• Extended Access Control (EAC)
• Supplemental Access Control – PACE v2
	Plain
	BAC
	EAC
	PACE
	  PACE-CAN
	  PACE-DH
	  PACE-ECDH
	  PACE-IM
	  PACE-GM
	  PACE-CAM[1]	

READ BINARY with odd instruction byte supported	OddIns	
eMRTD contains elementary file with LDS Data Group 3	DG3	
eMRTD contains elementary file with LDS Data Group 4	DG4	
eMRTD contains elementary file with LDS Data Group 5	DG5	
eMRTD contains elementary file with LDS Data Group 6	DG6	
eMRTD contains elementary file with LDS Data Group 7	DG7	
eMRTD contains elementary file with LDS Data Group 8	DG8	
eMRTD contains elementary file with LDS Data Group 9	DG9	
eMRTD contains elementary file with LDS Data Group 10	DG10	
eMRTD contains elementary file with LDS Data Group 11	DG11	
eMRTD contains elementary file with LDS Data Group 12	DG12	
eMRTD contains elementary file with LDS Data Group 13	DG13	
eMRTD contains elementary file with LDS Data Group 16	DG16	

Authentication supported:
• Passive Authentication
• Active Authentication
	ICAO
	AA
	  AA-RSA
	  AA-ECDSA	

MRZ provided with the samples	ICAO	
Country signing certificate (CSCA)	ICAO	
Document signer certificate, if not contained in SOD	ICAO	
Expected value for document type (2 characters)	ICAO	
Configuration list described in file EF.CardAccess (algorithm and domain parameters)	PACE	Please use annex to list all configurations.
Invalid key reference for PACE v2 (as used in test case ISO7816_P_09)	PACE	
Invalid password identifier for PACE v2 (as used in test case ISO7816_P_08)	PACE	
Invalid OID for PACE v2 (as used in test case ISO7816_P_68)	PACE	
Command to send to the eMRTD to verify the chip's ability to still require Secured APDU after performing valid or incomplete PACE v2 protocol. If not provided, use command "00 B0 81 00 00".	PACE	
IT security certification: • Certificate Number • Product Name • Date	N.A.	

[1] Chip Authentication Mapping

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Annex:

List of all PACE configurations:

Algorithm	Domain Parameter